



Normally-OFF Trench Silicon Carbide Power JFET

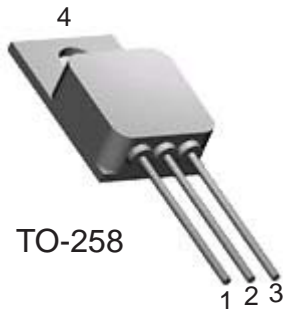
FEATURES:

SemiSouth Die Inside

- Hermetic TO-258 Packaging
- 200°C Maximum Operating Temperature (for 260°C Contact Factory)
- Available Screening:
 - MIL-PRF-19500 Equivalent
 - Space Level
 - MIL-STD-750 Methods & Conditions
- Inherent Radiation Tolerance >100K TID
- Compatible with Standard Gate Driver ICs
- Positive Temperature Coefficient for Ease of Paralleling
- Extremely Fast Switching with No "Tail" Current at 150°C
- 1200 Volt Drain-Source Blocking Voltage
- $RDS_{(on)max}$ of 0.100 Ω
- Voltage Controlled
- Low Gate Charge
- Low Intrinsic Capacitance

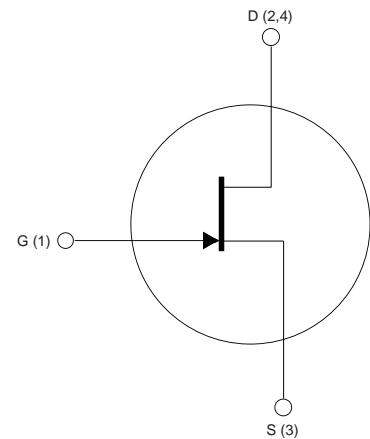
APPLICATIONS:

- Satellite Solar Inverters
- Mil Spec Power Supplies
 - Switch Mode
 - Uninterrupted
- Jet Engine Electronics
- Down-hole Electronics (Motor / Compressor Control)



TO-258

Product Summary		
BV_{DS}	1200	V
$RDS_{(ON)max}$	0.100	Ω
$E_{TS,typ}$	170	μJ



Internal Schematic

Non-isolated tab version shown. For isolated tab version, tab (4) is No Connect.

MAXIMUM RATINGS

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current	$I_D, T_j=125$	$T_j = 125\text{ }^\circ\text{C}$	17	A
	$I_D, T_j=175$	$T_j = 175\text{ }^\circ\text{C}$	12	
Pulsed Drain Current ⁽¹⁾	I_{DM}	$T_c = 25\text{ }^\circ\text{C}$	30	A
Short Circuit Withstand Time	t_{SC}	$V_{DD} < 800\text{ V}, T_c < 125\text{ }^\circ\text{C}$	50	μs
Power Dissipation	P_D	$T_c = 25\text{ }^\circ\text{C}$	136	W
Gate-Source Voltage	V_{GS}	AC ⁽²⁾	-15 to +15	V
Operating and Storage Temperature	$T_j, T_{j, stg}$		-55 to +200*	$^\circ\text{C}$
Lead Temperature for Soldering	T_{sold}	1/8" from case < 10 s	260	$^\circ\text{C}$

(1) Limited by pulse width

(2) $R_{gEXT} = 1\text{ ohm}, t_p < 200\text{ns}$, see Figure 5 for static conditions

* Contact Factory for 260°C

THERMAL CHARACTERISTICS

Parameter	Symbol	Value		Unit
		Typ	Max	
Thermal Resistance, junction-to-case	$R_{th,JC}$	-	TBD	$^\circ\text{C} / \text{W}$
Thermal Resistance, junction-to-ambient	$R_{th,JA}$	-	TBD	

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ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Conditions	Value			Unit
			Min	Typ	Max	
Off Characteristics						
Drain-Source Blocking Voltage	BV_{DS}	$V_{GS} = 0\text{ V}, I_D = 600\ \mu\text{A}$	1200	-	-	V
Total Drain Leakage Current	I_{DSS}	$V_{DS} = 1200\text{ V}, V_{GS} = 0\text{ V}, T_J = 25^\circ\text{C}$	-	100	600	μA
		$V_{DS} = 1200\text{ V}, V_{GS} = 0\text{ V}, T_J = 175^\circ\text{C}$	-	300	1000	
		$V_{DS} = 1200\text{ V}, V_{GS} = -15\text{ V}, T_J = 25^\circ\text{C}$	-	1	-	
		$V_{DS} = 1200\text{ V}, V_{GS} = -15\text{ V}, T_J = 175^\circ\text{C}$	-	10	-	
Total Gate Reverse Leakage	I_{GSS}	$V_{GS} = -15\text{ V}, V_{DS} = 0\text{ V}$	-	-0.1	-0.3	mA
		$V_{GS} = -15\text{ V}, V_{DS} = 1200\text{ V}$	-	-0.1	-	
On Characteristics						
Drain-Source On-resistance	$R_{DS(on)}$	$I_D = 12\text{ A}, V_{GS} = 3\text{ V}, T_J = 25^\circ\text{C}$	-	0.08	0.1	Ω
		$I_D = 12\text{ A}, V_{GS} = 3\text{ V}, T_J = 125^\circ\text{C}$	-	0.2	-	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = 1\text{ V}, I_D = 34\text{ mA}$	1.15	1.4	1.75	V
Gate Forward Current	I_{GFWD}	$V_{GS} = 3\text{ V}$	-	220	-	mA
Gate Resistance	R_G	$f = 1\text{ MHz}, \text{ drain-source shorted}$	-	8	-	Ω
	$R_{G(on)}$	$V_{GS} > 2.7\text{ V}; \text{ See Figure 5}$	-	0.5	-	Ω
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DD} = 100\text{ V}$	-	670	-	pF
Output Capacitance	C_{oss}		-	103	-	
Reverse Transfer Capacitance	C_{rss}		-	97	-	
Effective Output Capacitance, energy related	$C_{O(er)}$	$V_{DS} = 0\text{ V to } 480\text{ V}, V_{GS} = 0\text{ V}$	-	60	-	
Switching Characteristics						
Turn-On Delay	t_{on}	$V_{DS} = 600\text{ V}, I_D = 12\text{ A}, \text{ Inductive Load}, T_J = 25^\circ\text{C}$ Gate Driver = +15V, -10V, $R_{GEXT} = 50\text{ ohm}$ See Figure 15 for typical gate drive / inductive load switching circuit.	-	10	-	ns
Rise Time	t_r		-	12	-	
Turn-Off Delay	t_{off}		-	30	-	
Fall Time	t_f		-	25	-	
Turn-On Energy	E_{on}	See Figure 15 for typical gate drive / inductive load switching circuit.	-	70	-	μJ
Turn-Off Energy	E_{off}		-	100	-	
Total Switching Energy	E_{ts}		-	170	-	
Turn-On Delay	t_{on}		$V_{DS} = 600\text{ V}, I_D = 12\text{ A}, \text{ Inductive Load}, T_J = 150^\circ\text{C}$ Gate Driver = +15V, -10V, $R_{GEXT} = 50\text{ ohm}$ See Figure 15 for typical gate drive / inductive load switching circuit.	-	10	
Rise Time	t_r	-		15	-	
Turn-Off Delay	t_{off}	-		30	-	
Fall Time	t_f	-		25	-	
Turn-On Energy	E_{on}	See Figure 15 for typical gate drive / inductive load switching circuit.	-	85	-	μJ
Turn-Off Energy	E_{off}		-	100	-	
Total Switching Energy	E_{ts}		-	185	-	
Total Gate Charge	Q_g		$V_{DS} = 600\text{ V}, I_D = 10\text{ A}, V_{GS} = +2.5\text{ V}$	-	30	
Gate-Source Charge	Q_{gs}	-		1	-	
Gate-Drain Charge	Q_{gd}	-		24	-	



Figure 1. Typical Output Characteristics

$I_D = f(V_{DS}); T_j = 25\text{ }^\circ\text{C}; \text{parameter: } V_{GS}$

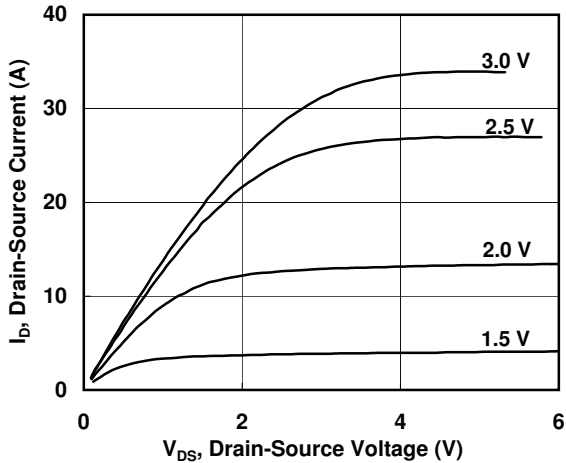


Figure 2. Typical Output Characteristics

$I_D = f(V_{DS}); T_j = 125\text{ }^\circ\text{C}; \text{parameter: } V_{GS}$

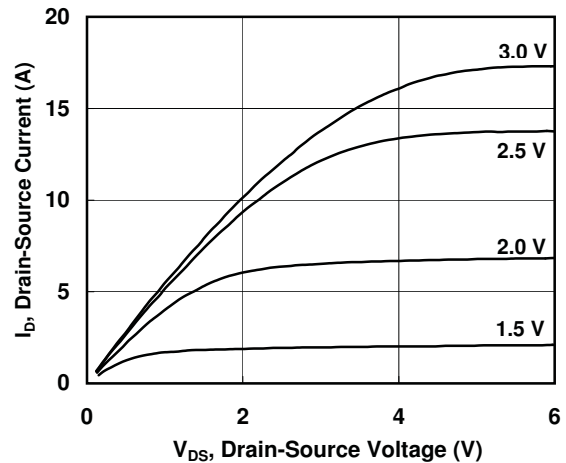


Figure 3. Typical Output Characteristics

$I_D = f(V_{DS}); T_j = 175\text{ }^\circ\text{C}; \text{parameter: } V_{GS}$

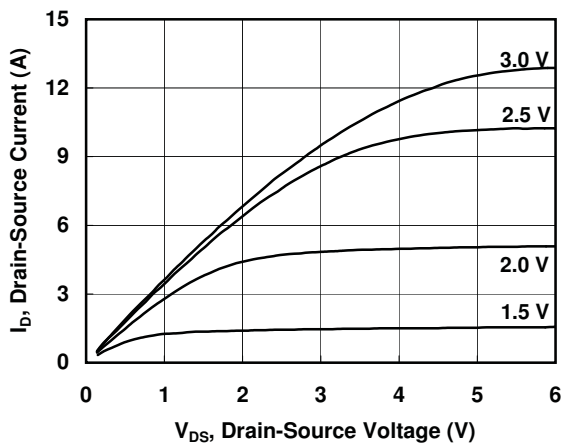


Figure 4. Typical Transfer Characteristics

$I_D = f(V_{GS}); V_{DS} = 5\text{ V}$

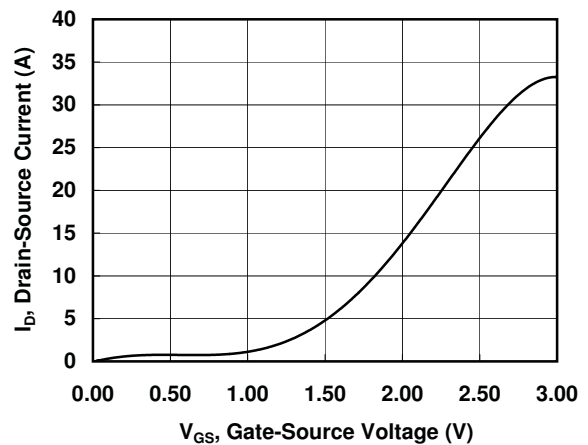


Figure 5. Gate-Source Current

$I_{GS} = f(V_{GS}); \text{parameter: } T_j$

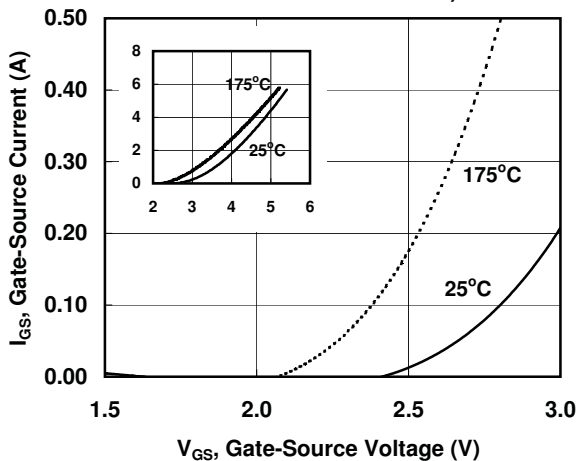


Figure 6. Drain-Source On-resistance

$R_{DS(on)} = f(I_D); V_{GS} = 3.0; \text{parameter: } T_j$

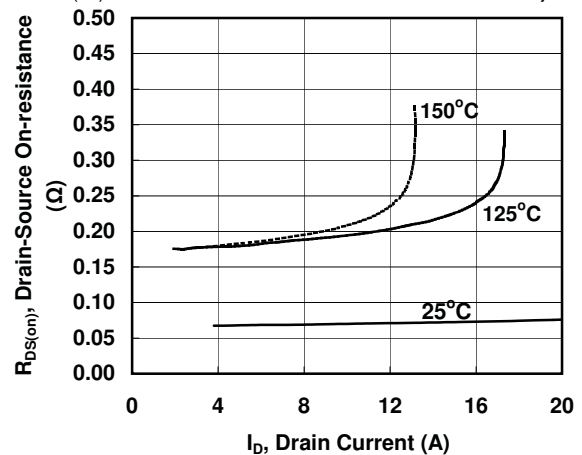




Figure 7. Drain-Source On-resistance

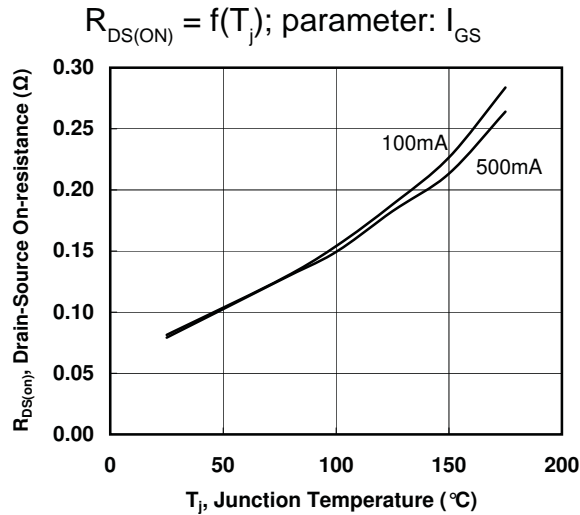


Figure 8. Drain-Source On-resistance

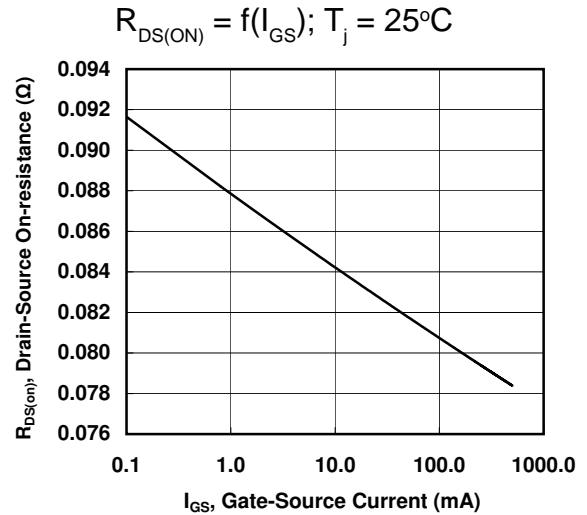


Figure 9. Typical Capacitance

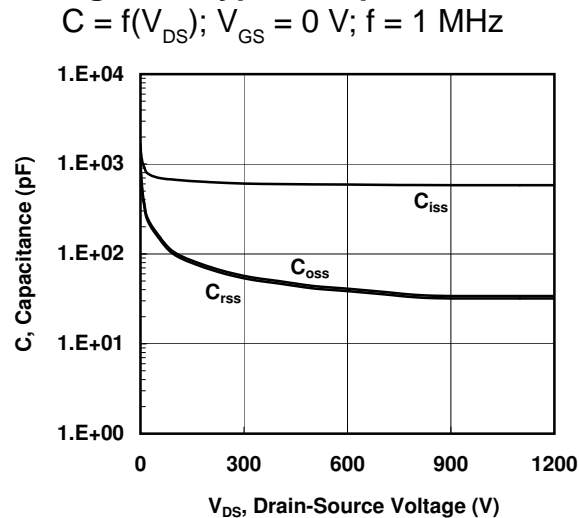


Figure 10. Gate Charge

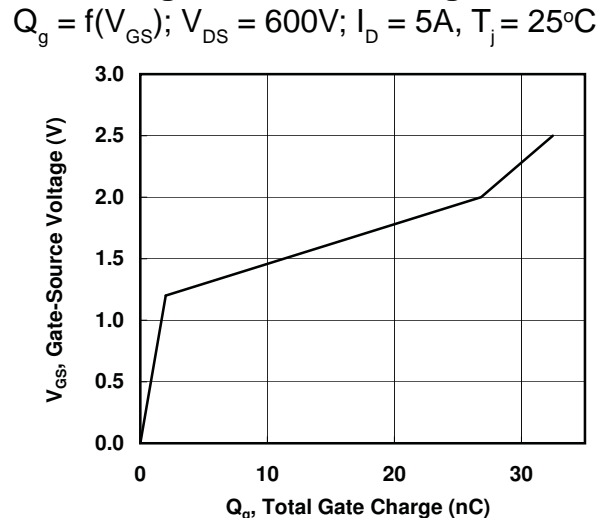


Figure 11. Gate Threshold Voltage

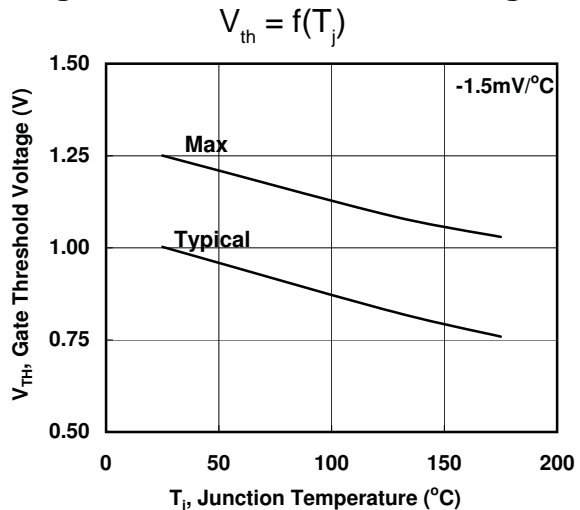


Figure 12. Drain-Source Leakage

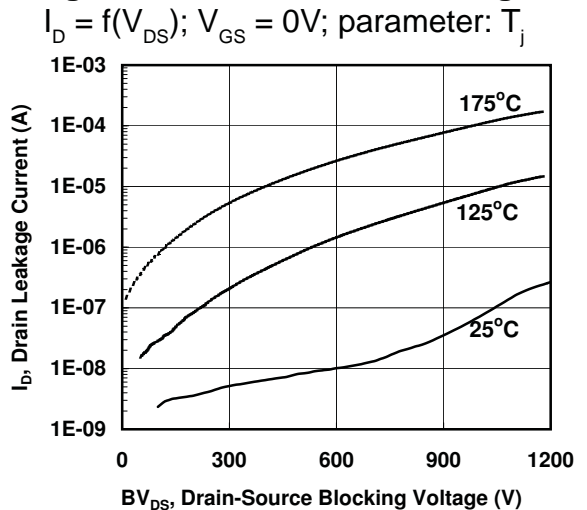




Figure 13. Switching Energy Losses

$E_s = f(I_D); V_{DS} = 600V; V_{GD} = +15V/-10V, R_{GEXT} = 50\Omega$

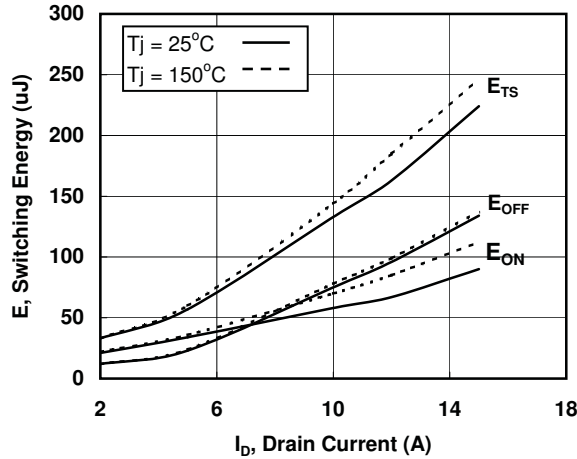


Figure 14. Switching Energy Losses

$E_s = f(R_{GEXT}); V_{DS} = 600V; I_D = 12A, V_{GD} = +15V/-10V$

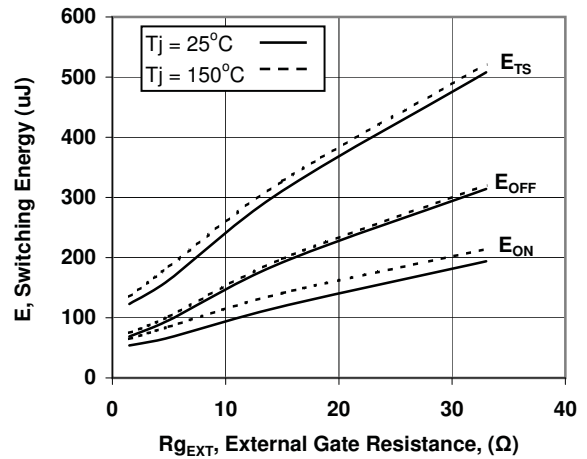


Figure 15. Inductive Load Switching Circuit

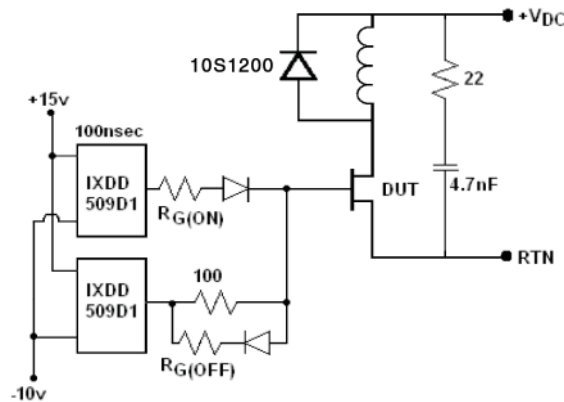
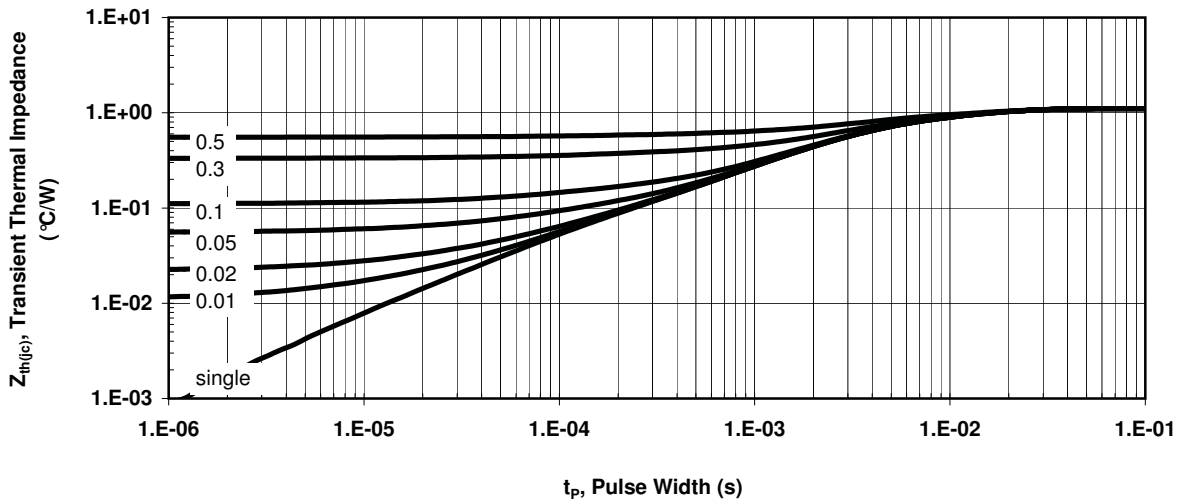


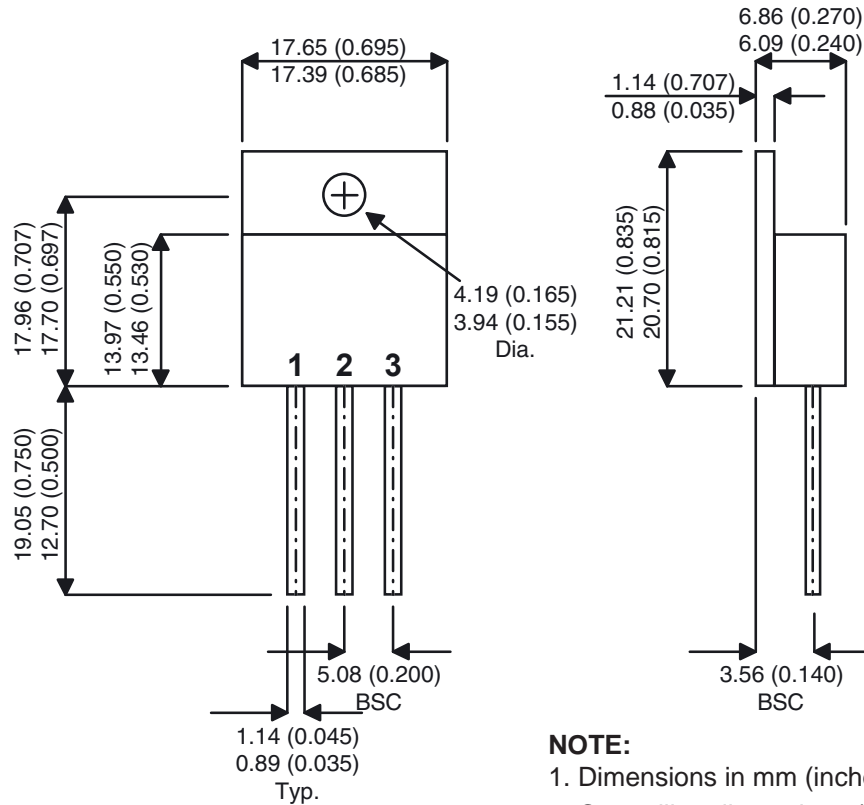
Figure 18. Transient Thermal Impedance

$Z_{th(jc)} = f(t_p); \text{parameter: Duty Ratio}$





MECHANICAL DRAWING



NOTE:
 1. Dimensions in mm (inches)
 2. Controlling dimensions (inches)

ORDERING INFORMATION

<u>Base Part Number</u>	<u>Configuration</u>	<u>Package</u>	<u>Junction Temp. Range</u>	<u>Processing</u>
ASJE1200R100	Blank= Non-isolated Tab S= Isolated Tab	M=TO-258 -	EL EX	Blank /V /S

Temp Ranges: EL= Elevated Temp. Range, -55°C to 200°C (T_j)
 EX= Extreme Temp. Range, -55°C to 260°C (T_j) (consult factory)

Processing: Blank = Commercial / Standard Processing
 MIL-PRF-19500 Equivalent Screening Available Per SCD
 /V= JANTX MIL-PRF-19500 Equivalent (future standard offering)
 /S= JANS MIL-PRF-19500 Equivalent (future standard offering)

Example Part Numbers: ASJE1200R100SM-EX
 ASJE1200R100M-EL

SemiSouth has commercial plastic versions of this product available. Please refer to the SemiSouth website <http://www.semisouth.com/products/products.html> for datasheet specifications and ordering information. The SemiSouth part number is SJEP120R100 and is supplied in a TO-247 plastic package.



DOCUMENT TITLE

Normally-OFF Trench Silicon Carbide Power JFET

<u>Rev #</u>	<u>History</u>	<u>Release Date</u>	<u>Status</u>
0.0	Initial Release	December 2010	Advance Information
0.1	Replaced TO-257 package with TO-258 package	June 2011	Advance Information