

=

=

=

1700 V

250 mΩ

8 A

D

 V_{DS}

R_{DS(ON)}

I_D

Normally – OFF Silicon Carbide Super Junction Transistor

Features

- 175 °C maximum operating temperature
- Temperature independent switching performance
- Gate oxide free SiC switch
- Suitable for connecting an anti-parallel diode
- Positive temperature coefficient for easy paralleling
- Low gate charge

Advantages

Higher efficiency

· Low switching losses

• High temperature operation

· High short circuit withstand capability

• Low intrinsic capacitance

Package

RoHS Compliant



TO-247AB

Applications

- Ideal for Aerospace and Defense Applications
- Down Hole Oil Drilling, Geothermal Instrumentation
- Hybrid Electric Vehicles (HEV)
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)
- Induction Heating
- Uninterruptible Power Supply (UPS)
- Motor Drives

Maximum Ratings at $T_i = 175$ °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Drain – Source Voltage	V _{DS}	$V_{GS} = 0 V$	1700	V
Continuous Drain Current	I _D	T _c = 90 °C	8	А
Gate Peak Current	I _{GM}		5	A
Reverse Gate – Source Voltage	V _{SG}		60	V
Reverse Drain – Source Voltage	V _{SD}		50	V
Power Dissipation	P _{tot}	T _C = 25 °C	16	W
Operating and Storage Temperature	T _j , T _{stg}		-55 to 175	°C

Electrical Characteristics at T_j = 175 °C, unless otherwise specified

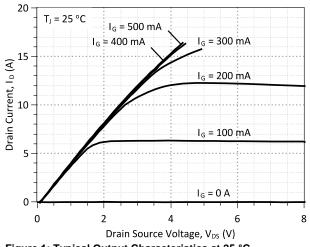
Deveryor	Cumula al	Conditions	Values		11	
Parameter	Symbol	Conditions -	min.	typ.	max.	Unit
On Characteristics						
Drain – Source On Voltage	V _{DS(ON)}	$I_D = 8 A, I_G = 500 mA, T_j = 25 °C$ $I_D = 8 A, I_G = 1000 mA, T_j = 125 °C$ $I_D = 8 A, I_G = 1000 mA, T_j = 175 °C$		2 3.3 4.5		V
Drain – Source On Resistance	R _{DS(ON)}	$I_D = 8 A, I_G = 500 mA, T_j = 25 °C$ $I_D = 8 A, I_G = 1000 mA, T_j = 125 °C$ $I_D = 8 A, I_G = 1000 mA, T_i = 175 °C$		250 400 550		mΩ
Gate Forward Voltage	$V_{GS(FWD)}$	I _G = 500 mA, T _j = 25 °C I _G = 500 mA, T _i = 175 °C		3 2.8		V
DC Current Gain	β	V _{DS} = 5 V, I _D = 8 A, T _j = 25 °C V _{DS} = 5 V, I _D = 8 A, T _j = 175 °C		65 40		
Off Characteristics						
Drain Leakage Current	I _{DSS}	$V_R = 1700 V, V_{GS} = 0 V, T_j = 25 °C$ $V_R = 1700 V, V_{GS} = 0 V, T_j = 125 °C$ $V_R = 1700 V, V_{GS} = 0 V, T_j = 175 °C$		0.1 0.5 2		μA



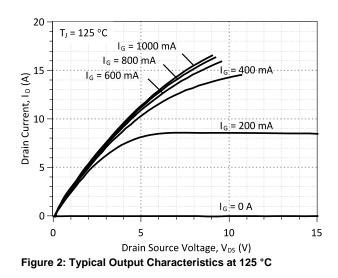
Electrical Characteristics at T_j = 175 °C, unless otherwise specified

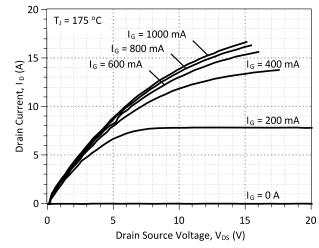
Parameter	Symbol Condi	Conditions		Values		L Incia
Farameter	Symbol	Conditions	min.	typ.	max.	Uni
Switching Characteristics						
Turn On Delay Time	t _{d(on)}			35		ns
Rise Time	t _r	$V_{DD} = 1100 \text{ V}, \text{ I}_{D} = 8 \text{ A},$		37		ns
Turn Off Delay Time	$t_{d(off)}$	$R_{G(on)} = R_{G(off)} = 44 \Omega,$ V _{GS} = -8/15 V, L = 1.1 mH,		45		ns
Fall Time	t _f	FWD = GA12SHT12.		38		ns
Turn-On Energy Per Pulse	Eon	$T_j = 25 ^{\circ}C$ Refer to Figure 11 for gate current		678		μJ
Turn-Off Energy Per Pulse	E _{off}			24		μJ
Total Switching Energy	E _{ts}	waveform		702		μJ
Turn On Delay Time	t _{d(on)}			28		
Rise Time	tr	$\label{eq:VDD} \begin{array}{l} V_{DD} = 1100 \ V, \ I_D = 8 \ A, \\ R_{G(on)} = R_{G(off)} = 44 \ \Omega, \\ V_{GS} = -8/15 \ V, \ L = 1.1 \ mH, \\ FWD = GA12SHT12, \\ T_j = 175 \ ^{\circ}C \\ Refer to \ Figure \ 11 \ for \ gate \ current \\ waveform \end{array}$		25		ns
Turn Off Delay Time	$t_{d(off)}$			44		ns
Fall Time	t _f			33		ns
Turn-On Energy Per Pulse	Eon			495		μJ
Turn-Off Energy Per Pulse	E _{off}			26		μJ
Total Switching Energy	E _{ts}	waveloitti		521		μJ

Thermal resistance, junction - case	R _{thJC}	1.03	°C/W



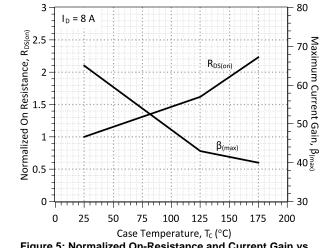




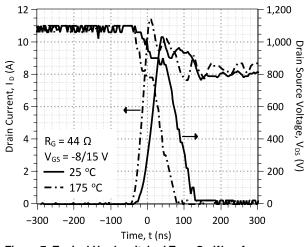


DU

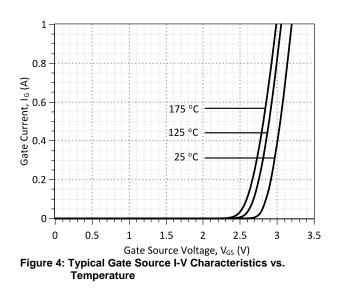
Figure 3: Typical Output Characteristics at 175 °C

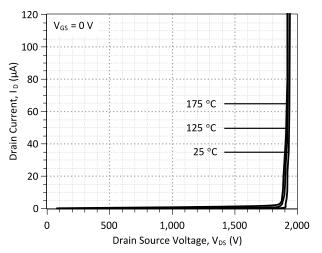


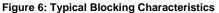


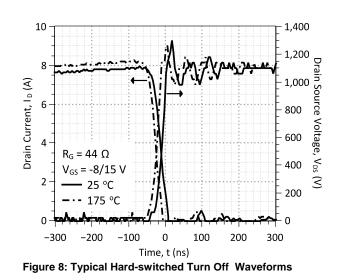




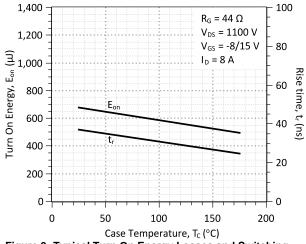


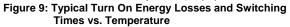






GA08JT17-247





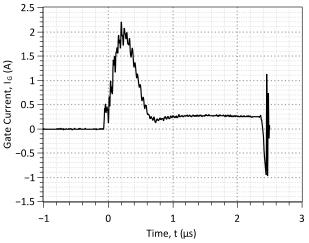


Figure 11: Typical Gate Current Waveform

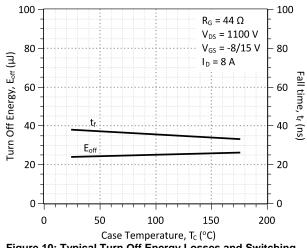
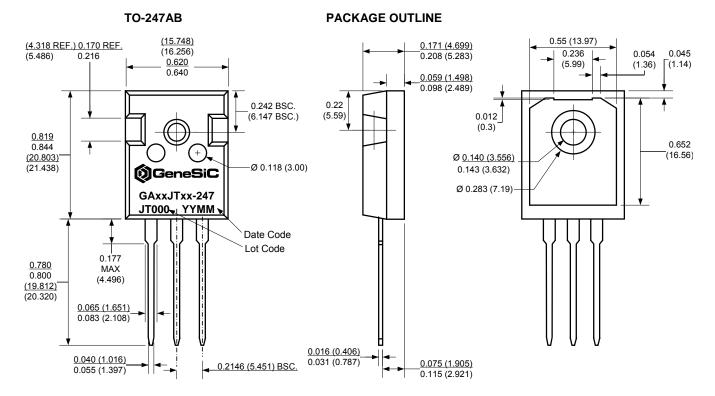


Figure 10: Typical Turn Off Energy Losses and Switching Times vs. Temperature



Package Dimensions:



NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.

2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

Revision History					
Date	Revision	Comments	Supersedes		
2012/09/26	0	Initial release			

Published by GeneSiC Semiconductor, Inc. 43670 Trade Center Place Suite 155 Dulles, VA 20166

GeneSiC Semiconductor, Inc. reserves right to make changes to the product specifications and data in this document without notice.

GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Unless otherwise expressly indicated, GeneSiC products are not designed, tested or authorized for use in life-saving, medical, aircraft navigation, communication, air traffic control and weapons systems, nor in applications where their failure may result in death, personal injury and/or property damage.