

## Normally – OFF Silicon Carbide Super Junction Transistor

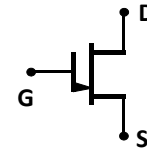
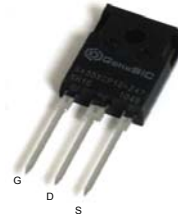
$V_{DS}$	=	1700 V
$I_D$	=	8 A
$R_{DS(ON)}$	=	250 mΩ

### Features

- 175 °C maximum operating temperature
- Temperature independent switching performance
- Gate oxide free SiC switch
- Suitable for connecting an anti-parallel diode
- Positive temperature coefficient for easy paralleling
- Low gate charge
- Low intrinsic capacitance

### Package

- RoHS Compliant


**TO-247AB**

### Advantages

- Low switching losses
- Higher efficiency
- High temperature operation
- High short circuit withstand capability

### Applications

- Ideal for Aerospace and Defense Applications
- Down Hole Oil Drilling, Geothermal Instrumentation
- Hybrid Electric Vehicles (HEV)
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)
- Induction Heating
- Uninterruptible Power Supply (UPS)
- Motor Drives

### Maximum Ratings at $T_j = 175\text{ °C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Drain – Source Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}$	1700	V
Continuous Drain Current	$I_D$	$T_C = 90\text{ °C}$	8	A
Gate Peak Current	$I_{GM}$		5	A
Reverse Gate – Source Voltage	$V_{SG}$		60	V
Reverse Drain – Source Voltage	$V_{SD}$		50	V
Power Dissipation	$P_{tot}$	$T_C = 25\text{ °C}$	16	W
Operating and Storage Temperature	$T_j, T_{stg}$		-55 to 175	°C

### Electrical Characteristics at $T_j = 175\text{ °C}$ , unless otherwise specified

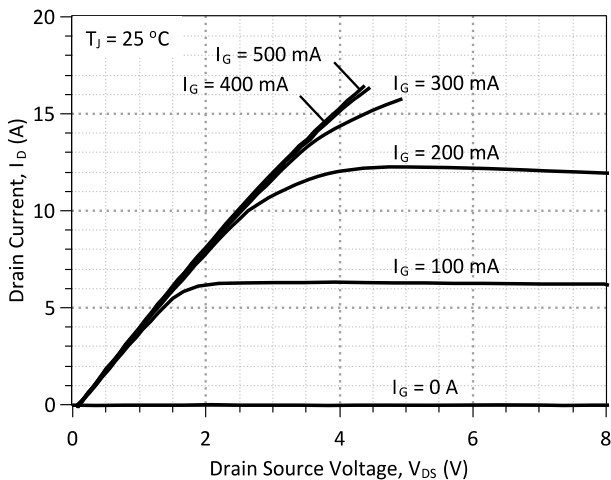
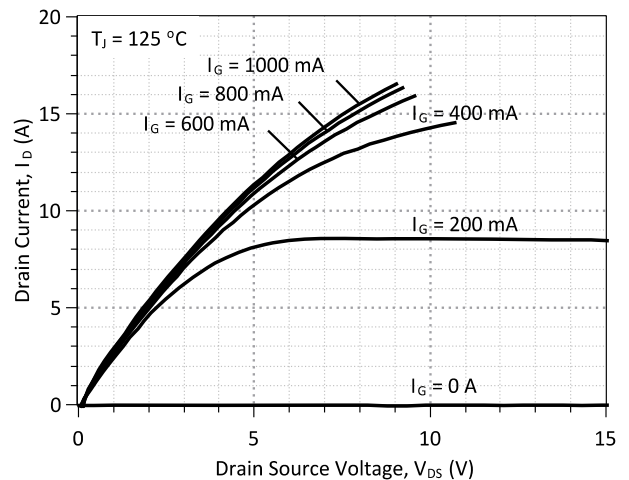
Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
<b>On Characteristics</b>						
Drain – Source On Voltage	$V_{DS(ON)}$	$I_D = 8\text{ A}, I_G = 500\text{ mA}, T_j = 25\text{ °C}$		2		V
		$I_D = 8\text{ A}, I_G = 1000\text{ mA}, T_j = 125\text{ °C}$		3.3		
		$I_D = 8\text{ A}, I_G = 1000\text{ mA}, T_j = 175\text{ °C}$		4.5		
Drain – Source On Resistance	$R_{DS(ON)}$	$I_D = 8\text{ A}, I_G = 500\text{ mA}, T_j = 25\text{ °C}$		250		mΩ
		$I_D = 8\text{ A}, I_G = 1000\text{ mA}, T_j = 125\text{ °C}$		400		
		$I_D = 8\text{ A}, I_G = 1000\text{ mA}, T_j = 175\text{ °C}$		550		
Gate Forward Voltage	$V_{GS(FWD)}$	$I_G = 500\text{ mA}, T_j = 25\text{ °C}$ $I_G = 500\text{ mA}, T_j = 175\text{ °C}$		3 2.8		V
DC Current Gain	$\beta$	$V_{DS} = 5\text{ V}, I_D = 8\text{ A}, T_j = 25\text{ °C}$		65		
		$V_{DS} = 5\text{ V}, I_D = 8\text{ A}, T_j = 175\text{ °C}$		40		
<b>Off Characteristics</b>						
Drain Leakage Current	$I_{DSS}$	$V_R = 1700\text{ V}, V_{GS} = 0\text{ V}, T_j = 25\text{ °C}$		0.1		μA
		$V_R = 1700\text{ V}, V_{GS} = 0\text{ V}, T_j = 125\text{ °C}$		0.5		
		$V_R = 1700\text{ V}, V_{GS} = 0\text{ V}, T_j = 175\text{ °C}$		2		

**Electrical Characteristics at  $T_j = 175\text{ }^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
<b>Switching Characteristics</b>						
Turn On Delay Time	$t_{d(on)}$	$V_{DD} = 1100\text{ V}$ , $I_D = 8\text{ A}$ , $R_{G(on)} = R_{G(off)} = 44\ \Omega$ , $V_{GS} = -8/15\text{ V}$ , $L = 1.1\text{ mH}$ , FWD = GA12SHT12, $T_j = 25\text{ }^\circ\text{C}$		35		ns
Rise Time	$t_r$			37		ns
Turn Off Delay Time	$t_{d(off)}$			45		ns
Fall Time	$t_f$			38		ns
Turn-On Energy Per Pulse	$E_{on}$				678	
Turn-Off Energy Per Pulse	$E_{off}$	Refer to Figure 11 for gate current waveform		24		$\mu\text{J}$
Total Switching Energy	$E_{ts}$			702		$\mu\text{J}$
Turn On Delay Time	$t_{d(on)}$	$V_{DD} = 1100\text{ V}$ , $I_D = 8\text{ A}$ , $R_{G(on)} = R_{G(off)} = 44\ \Omega$ , $V_{GS} = -8/15\text{ V}$ , $L = 1.1\text{ mH}$ , FWD = GA12SHT12, $T_j = 175\text{ }^\circ\text{C}$		28		
Rise Time	$t_r$			25		ns
Turn Off Delay Time	$t_{d(off)}$			44		ns
Fall Time	$t_f$			33		ns
Turn-On Energy Per Pulse	$E_{on}$				495	
Turn-Off Energy Per Pulse	$E_{off}$	Refer to Figure 11 for gate current waveform		26		$\mu\text{J}$
Total Switching Energy	$E_{ts}$			521		$\mu\text{J}$

**Thermal Characteristics**

Thermal resistance, junction - case	$R_{thJC}$	1.03	$^\circ\text{C/W}$
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**Figure 1: Typical Output Characteristics at 25 °C**

**Figure 2: Typical Output Characteristics at 125 °C**

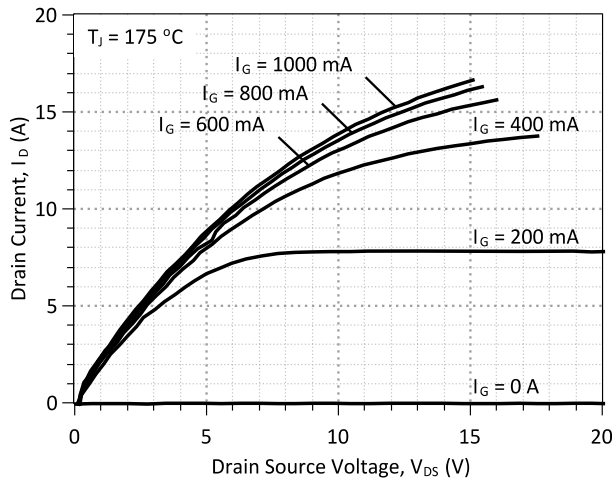


Figure 3: Typical Output Characteristics at 175 °C

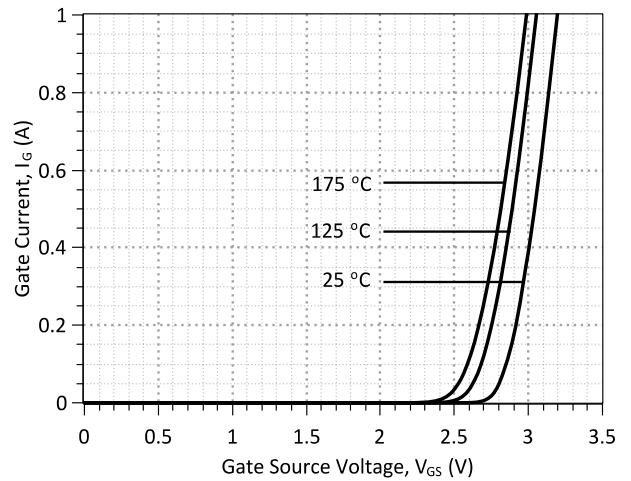


Figure 4: Typical Gate Source I-V Characteristics vs. Temperature

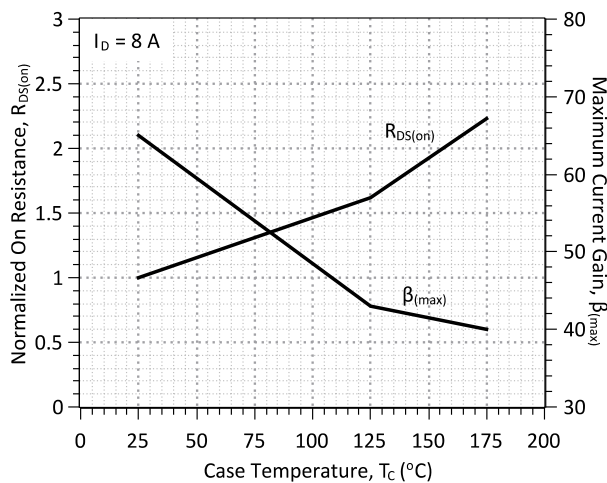


Figure 5: Normalized On-Resistance and Current Gain vs. Temperature

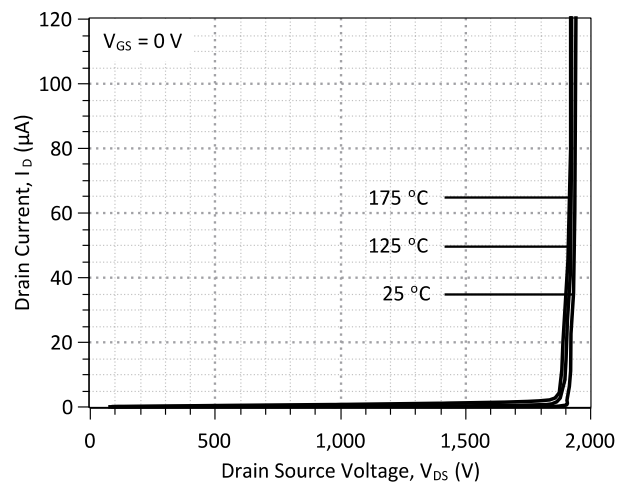


Figure 6: Typical Blocking Characteristics

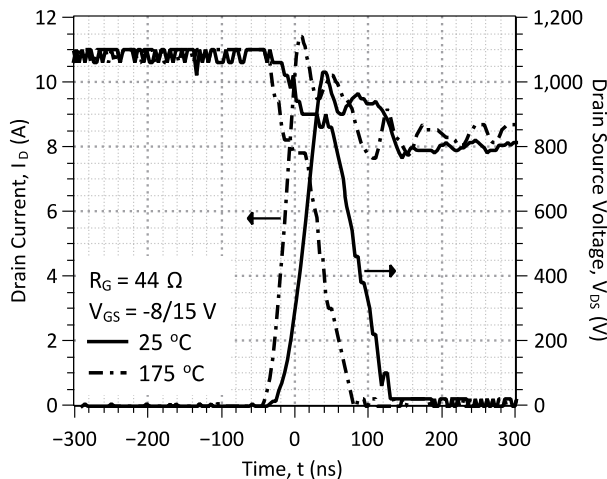


Figure 7: Typical Hard-switched Turn On Waveforms

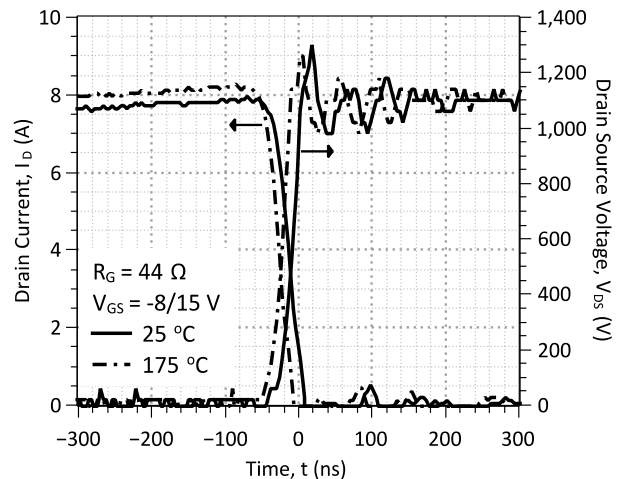


Figure 8: Typical Hard-switched Turn Off Waveforms

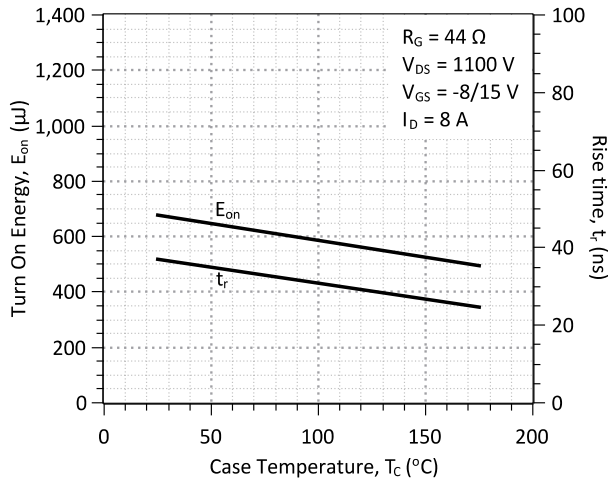


Figure 9: Typical Turn On Energy Losses and Switching Times vs. Temperature

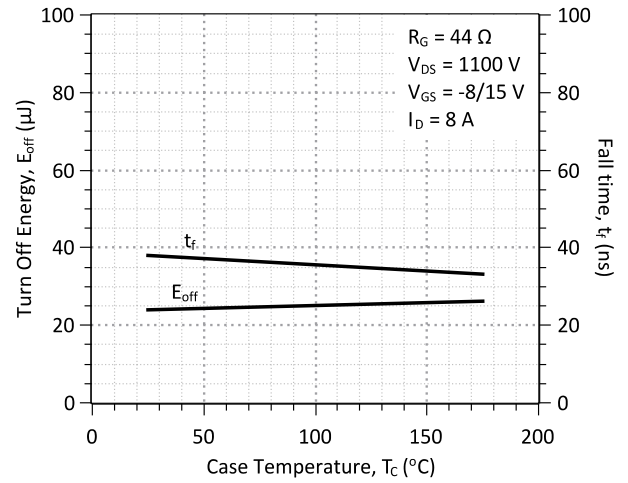


Figure 10: Typical Turn Off Energy Losses and Switching Times vs. Temperature

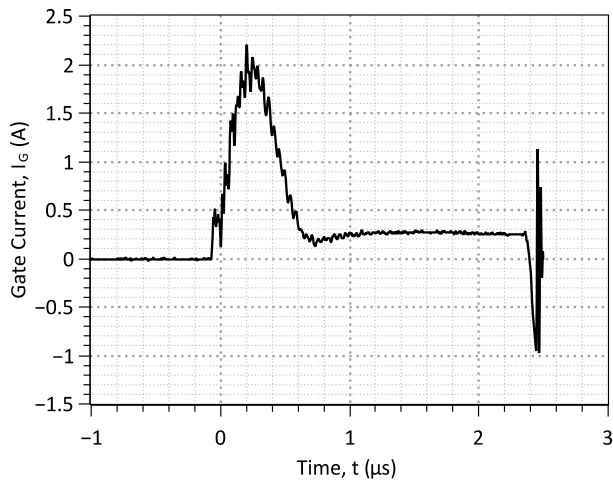


Figure 11: Typical Gate Current Waveform

