Preferred Device

Amplifier Transistors PNP Silicon

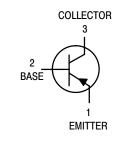
Features

• Pb–Free Packages are Available*



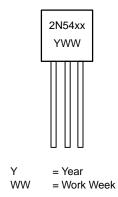
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Preferred devices are recommended choices for future use and best overall value.

MAXIMUM RATINGS

Rating	Symbol	2N5400	2N5401	Unit
Collector – Emitter Voltage	V _{CEO}	120	150	Vdc
Collector – Base Voltage	V _{CBO}	_O 130 160		Vdc
Emitter – Base Voltage	V _{EBO}	5.0		Vdc
Collector Current – Continuous	Ι _C	600		mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	62 5		mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12		Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	–55 to) +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25° C unless otherwise noted)

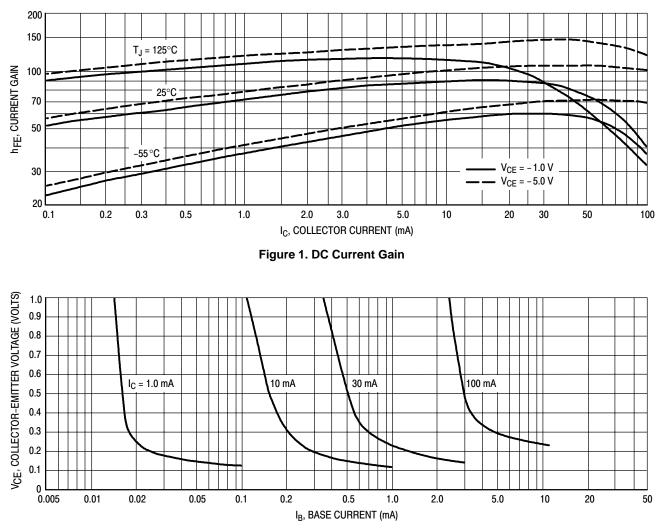
Symbol	Min	Max	Unit
		1	1
	150	_	Vdc
	160	_	Vdc
V _{(BR)EBO}	5.0	-	Vdc
		50 50	
I _{EBO}	_	50	nAdc
		1	1
h _{FE}	50 60 50	_ 240 _	_
V _{CE(sat)}		0.2 0.5	Vdc
V _{BE(sat)}		1.0 1.0	Vdc
fT	100	300	MHz
C _{obo}	-	6.0	pF
h _{fe}	40	200	-
NF	-	8.0	dB
	V(BR)CEO 00 V(BR)CBO 01 V(BR)CBO 01 V(BR)EBO 01 ICBO 01 IEBO VCE(sat) VCE(sat) VBE(sat) T fT Cobo hfe Ke	$\begin{array}{c c c c c c c c } & V_{(BR)CEO} & 150 \\ \hline \\ 00 \\ 01 & V_{(BR)CBO} & 160 \\ \hline \\ 01 & V_{(BR)EBO} & 5.0 \\ \hline \\ 01 & I_{CBO} & - \\ \hline \\ 01 & I_{CO} & - \\ \hline \\ 01 & I_{CO}$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

1. Pulse Test: Pulse Width $\leq 300~\mu s,$ Duty Cycle $\leq 2.0\%.$

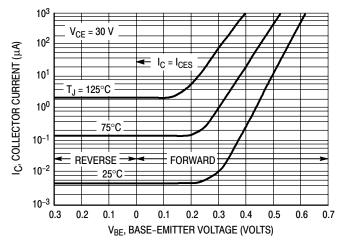
ORDERING INFORMATION

Device	Package	Shipping [†]	
2N5401	TO-92	5000 Unit / Bulk	
2N5401RL1	TO-92	2000 Tape & Reel	
2N5401RLRA	TO-92	2000 Tape & Reel	
2N5401RLRAG	TO-92 (Pb-Free)	2000 Tape & Reel	
2N5401RLRM	TO-92	2000 Tape & Ammo Box	
2N5401ZL1	TO-92	2000 Tape & Ammo Box	

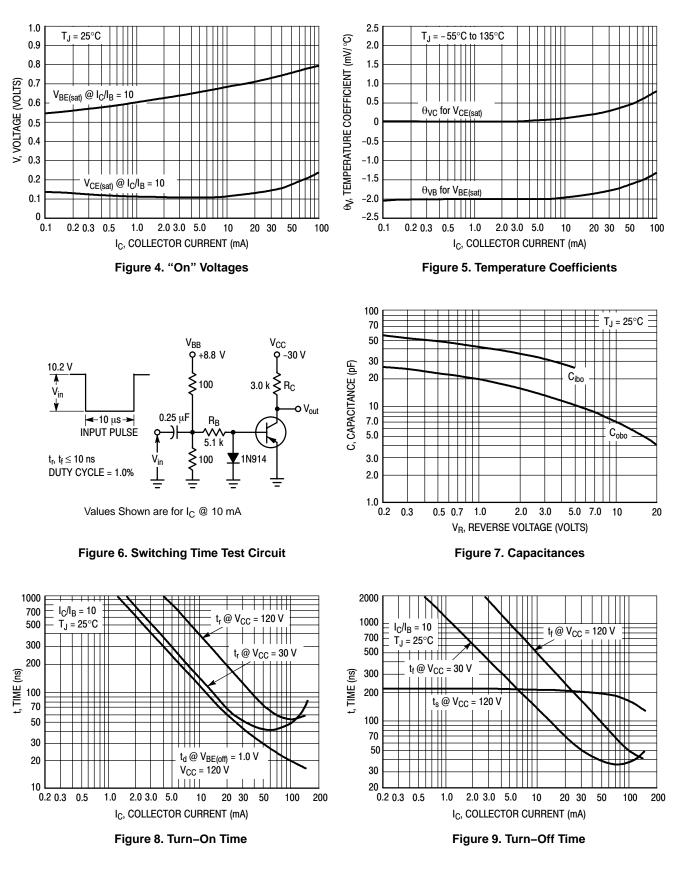
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.





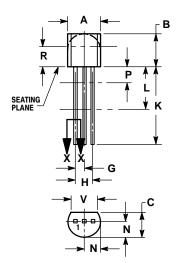






PACKAGE DIMENSIONS

TO-92 CASE 29-11 **ISSUE AL**





NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0.135		3.43	

STYLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under tits patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to rusport or usuatin life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use all such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2–9–1 Kamimeguro, Meguro–ku, Tokyo, Japan 153–0051 Phone: 81–3–5773–3850 ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.